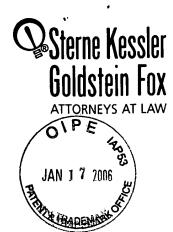
09/493, 942.



Robert Greene Sterne Edward J. Kessler Jorge A. Goldstein David K.S. Cornwell Robert W. Esmond Tracy-Gene G. Durkin Michele A. Cimbala Michael B. Ray Eric K. Steffe Michael Q. Lee Steven R. Ludwig John M. Covert Linda E. Alcom Donald J. Featherstone Timothy J. Shea, Jr Michael V. Messinger

Jeffrey T. Helvey Heidi L. Kraus Eldora L. Ellison Thomas C. Fiala Donald R. Banowit Peter A. Jackman Jeffrey S. Weaver Kendrick P. Patterson Vincent L. Capuano Brian J. Del Buono Theodore A. Wood Joseph S. Ostroff Rae Lynn P. Guest

Jason D. Eisenberg Michael D. Specht Tracy L. Muller Jon E. Wright LuAnne M. DeSantis Ann E. Summerfield Helene C. Carlson Cynthia M. Bouchez Timothy A. Doyle Gaby L. Longsworth Lori A. Gordon Nicole D. Dretar Laura A. Vogel Bryan S. Wade Aaron L. Schwartz Shannon A. Carroll'

Wesley W. Jones\* Matthew E. Kelley\* Michelle K. Holoubek\* Karen R. Markowicz

Registered Patent Agents • Nancy J. Leith Matthew J. Dowd Katrina Yujian Pei Quach Bryan L. Skelton Robert A. Schwartzman Teresa A. Colella Victoria S. Rutherford Simon J. Elliott Julie A. Heider Mita Mukherjee Scott M. Woodhouse

Liliana Di Nola-Baron Peter A Socarras Jeffrey Mills Danielle L. Letting

Of Counsel Kenneth C. Bass III Marvin C. Guthrie

\*Admitted only in Maryland Admitted only in Virginia Practice Limited to Federal Agencies

January 17, 2006

WRITER'S DIRECT NUMBER: (202) 772-8675 INTERNET ADDRESS: JHELVEY@SKGF.COM

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450 Art Unit 2832

Attn: Certification of Corrections Branch

Re:

U.S. Patent No. 6,885,275 B1; Issued: April 26, 2005

**Multi-Track Integrated Spiral Inductor** 

Inventor: Our Ref:

James Y.C. Chang 1875.1380006

Certificate

JAN 2 4 2006

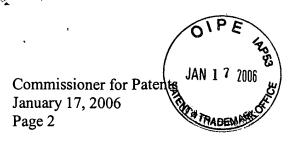
of Correction

Sir:

Transmitted herewith for appropriate action are the following documents:

- 1. Credit Card Payment Form (PTO-2038) for \$100.00 to cover Certificate of Correction Fee Under 37 C.F.R. § 1.20(a);
- 2. Request for Certificate of Correction Under 37 C.F.R. §§ 1.322 and 1.323;
- 3. Exhibit 1 A copy of the Corrected Filing Receipt dated October 5, 2000;
- 4. Exhibit 2 A copy of page 66 of the original application as filed on January 28, 2000, along with a copy of the postcard barcode-stamped by the U.S. Patent and Trademark Office;
- 5. Certificate of Correction Form PTO/SB/44; and
- 6. One return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier.



The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Jeffrey T. Helvey Attorney for Patentee Registration No. 44,757

JTH/BSW/srb:sjc Enclosures

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Steme, Kessler, Goldstein & Fox PLLC : 1100 New York Avenue, NW: Washington, DC 20005: 202.371.2600 f 202.371.2540: www.skgf.com



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent of:

James Y.C. Chang

Patent. No.: 6,885,275 B1

Issued: April 26, 2005

For: Multi-Track Integrated Spiral

**Inductor** 

Confirmation No.: 2849

Art Unit: 2832

Examiner: Donovan, Lincoln D.

Atty. Docket: 1875.1380006

01/19/2006 MDAMTE1 00000003 6885275

01 FC:1811 100.00 0P

Request for Certificate of Correction Under 37 C.F.R. §§ 1.322 and 1.323

Attn: Certificate of Correction Branch

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

It is hereby requested that a Certificate of Correction under 37 C.F.R. §§ 1.322 and 1.323 be issued for the above-captioned United States Patent. This Certificate of Correction is being requested due to mistakes which appear in the printed patent. These mistakes were made both by the U.S. Patent and Trademark Office and by Applicant. The mistakes made by Applicant are of a clerical or typographical nature, or of a minor character. Patentee(s) submit that correction of these errors does not introduce new matter.

Specifically, the printed patent contains the following errors for which a Certificate of Correction is respectfully requested:

On the cover page, in Section (63), the filing date of priority application no. 09/439,101, should be changed from "Nov. 12, 1998" to --Nov. 12, 1999--. This correction is requested to correct a typographical error made by the USPTO. A copy of the Corrected Filing Receipt dated October 5, 2000 is enclosed in support of this correction as Exhibit 1.

In column 34, lines 25-28, in Equation (5), replace " $(C_2)$ " with -- $(C_2)$ --, and replace " $(C_1)$ " with -- $(C_1)$ --. This correction is requested to correct a typographical error made by Applicants. A copy of page 66 of the original application as filed on January 28, 2000,

James Y.C. Chang Patent. No. 6,885,275

along with a copy of the postcard barcode-stamped by the U.S. Patent and Trademark Office, is enclosed in support of this correction as Exhibit 2.

### Remarks

The above-noted corrections do not involve such changes in the patent as would constitute new matter or would require reexamination.

A completed Form PTO/SB/44 accompanies this request, with the above-noted corrections printed thereon. Accordingly, a Certificate of Correction is believed proper and issuance thereof is respectfully requested.

This request is accompanied by payment of the fee set forth in 37 C.F.R. § 1.20(a). Fee payment is provided in the attached Credit Card Payment Form (PTO-2038). The Commissioner is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Jeffrey T. Helvey Attorney for Patentee Registration No. 44,757

1/17/06

1100 New York Avenue, N.W. Washington, D.C. 20005-3934 (202) 371-2600

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# Exhibit 1







COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 2023I

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS	
09/493,942	01/28/2000	2811	1264	86491/LTR/B600	68	23	8	

Christie Parker & Hale LLP P.O. Box 7068 Pasadena, CA 91109-7068 B600: 36491 REMINION

CORRECTED FILING RECEIPT

\*OC000000005456171\*

Date Mailed: 10/05/2000

Receipt is acknowledged of this comprovisional Patent Application, it will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

James Y.C. Chang, Irvine, CA;

### Continuing Data as Claimed by Applicant

THIS APPLICATION IS A CIP OF 09/483,551 01/14/2000 WHICH IS A CIP OF 09/439,101 11/12/1999 WHICH CLAIMS BENEFIT OF 60/108,459 11/12/1998 AND CLAIMS BENEFIT OF 60/108,209 11/12/1998 AND CLAIMS BENEFIT OF 60/108,210 11/12/1998 AND CLAIMS BENEFIT OF 60/117,609 01/28/1999 AND CLAIMS BENEFIT OF 60/136,115 05/26/1999 AND CLAIMS BENEFIT OF 60/136,654/05/27/1999 AND CLAIMS BENEFIT OF 60/159,726 10/15/1999 AND CLAIMS BENEFIT OF 60/116,003 01/15/1999 AND CLAIMS BENEFIT OF 60/117,322 01/26/1999 AND CLAIMS BENEFIT OF 60/117,322 01/26/1999 AND CLAIMS BENEFIT OF 60/122,754 02/25/1999

### **Foreign Applications**

If Required, Foreign Filing License Granted 03/30/2000

**Title** 

Multi-track integrated spiral inductor

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PROV Claimed? MULTI-TRACK INTEGRATED SPIRAL INDUCTOR

Assignment Enclosed (List Assignee)

Check(s)

	OTHER Fee Transmitted (2)	PCT	Petition Extension of Time	Info Disc Stmt/PTO-1449	Small Entity Declaration	3 Specimens/Photos/Facsimiles	Artomyal/Formal Drawings		ages)	mansmittal Letter(s)
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# **APPLICATION**

Express Mail No. EL521383128US Date Mailed Client ID Atty/Sec Case No 8600 36491 LTR/PBH/11w /28/2000

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**Patent** 

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**Parent Case** FOR REV/DEADLINE \_ 44e

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scaling of the capacitors is necessary. The frequency relation in the exemplary embodiment is given by the ratio:

$$\frac{1}{2} \approx \sqrt{\frac{(L_2) (C_2)}{(L_1) (C_1)}} \tag{5}$$

For this particular embodiment utilizing identical inductor values  $L_1 = L_2$ . This reduces to:

$$\frac{f_1}{f_2} \approx \sqrt{\frac{(C_2)}{(C_1)}} \tag{6}$$

For the exemplary embodiment this is equal to 925/1200, or a capacitance ratio of 3:5. However, it is understood that other ratios will allow tuning to be performed equivalently.

Returning to FIG. 23a various control signals applied to the tuning circuit are shown. In the event that the tuning is slightly off after the tuning procedure, an offset control circuit is provided within the tuning circuit of FIG. 23 to move the tuning of the filters up or down slightly by providing a manual means of adding or removing a capacitor. This control is shown by an "up/down" control line 2324 of FIG. 23a. The exemplary tuning circuit of FIG. 23 is additionally provided with a "LO" 2308 tuning frequency to tune the dummy filter. The "10 MHz reference" signal 2326 is utilized as a clock in the tuning circuit that controls the sequence of adding capacitors. The "reset" signal 2328 resets the tuning circuit for the next tuning cycle.

FIG. 25 is a block diagram of an exemplary tuning circuit. A reset signal 2502 is utilized to eliminate all the capacitors from the circuit at power up by resetting a counter 2504 that controls the application of the switched capacitors. The reset signal may be initiated by a controller or generated locally. This provides a known starting point for filter tuning. Next a

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(Also Form PTO-1050)

# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO: 6,885,275 B1

DATED:

April 26, 2005

INVENTOR(S): James Y.C. Chang

It is certified that error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below.

On the cover page, in Section (63), the filing date of priority application no. 09/439,101, should be changed from "Nov. 12, 1998" to --Nov. 12, 1999--.

In column 34, lines 25-28, in Equation (5), replace " $(C_2)$ " with -- $(C_2)$ --, and replace " $(C_1)$ " with -- $(C_1)$ --.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

1100 New York Avenue, NW Washington DC 20005-3934 Atty. Dkt. No. 1875.1380006

This collection of information is required by 37 CFR 1.322, 1.323 and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.